



4



TRANSFER SWITCH SCHEMATIC

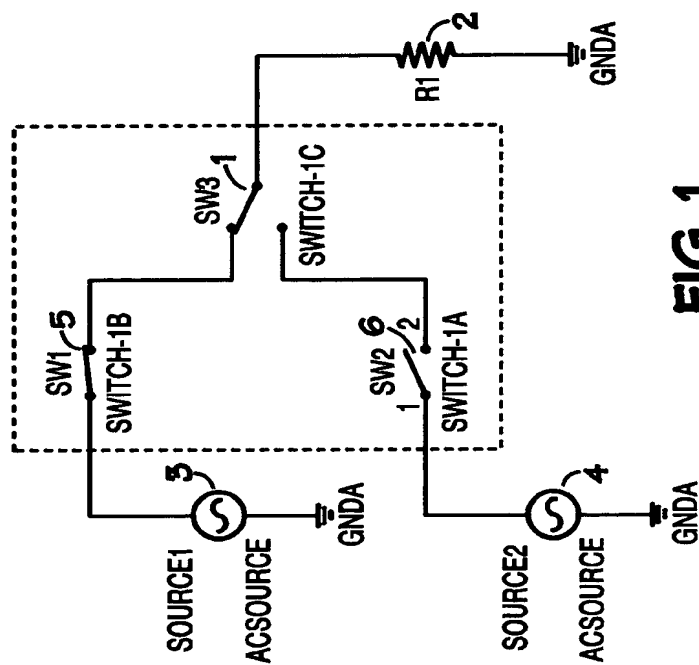
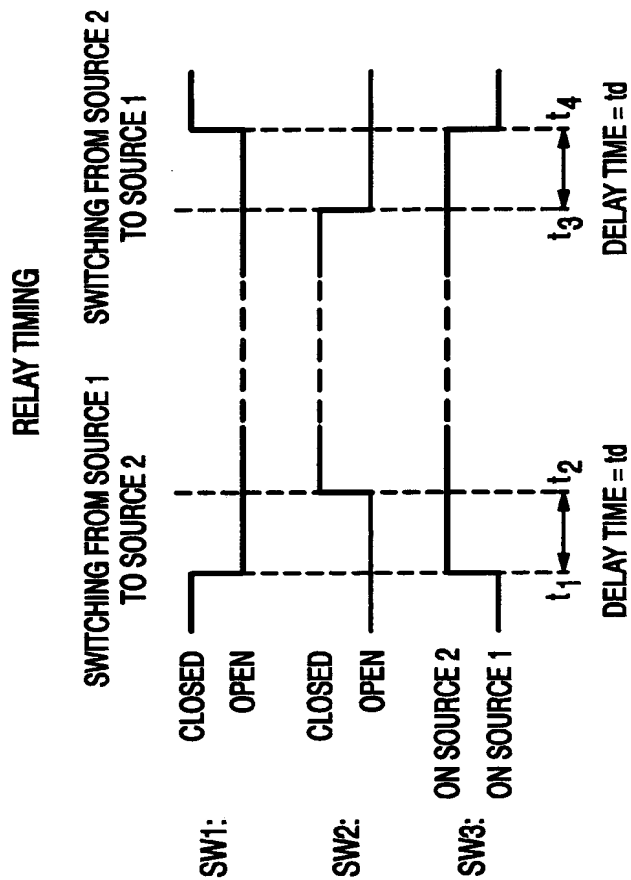
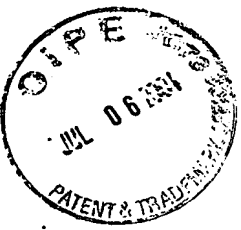


FIG. 1

FIG. 2





4

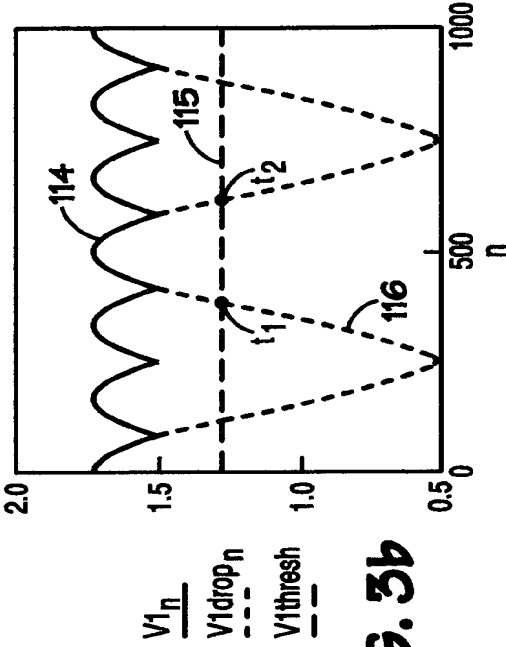
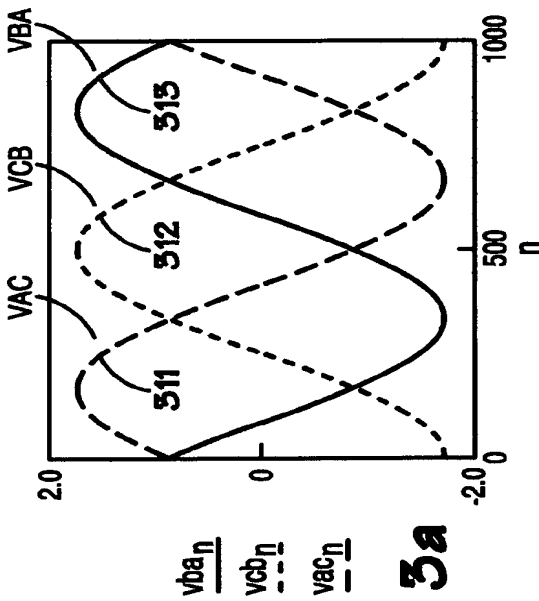
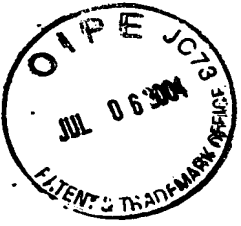
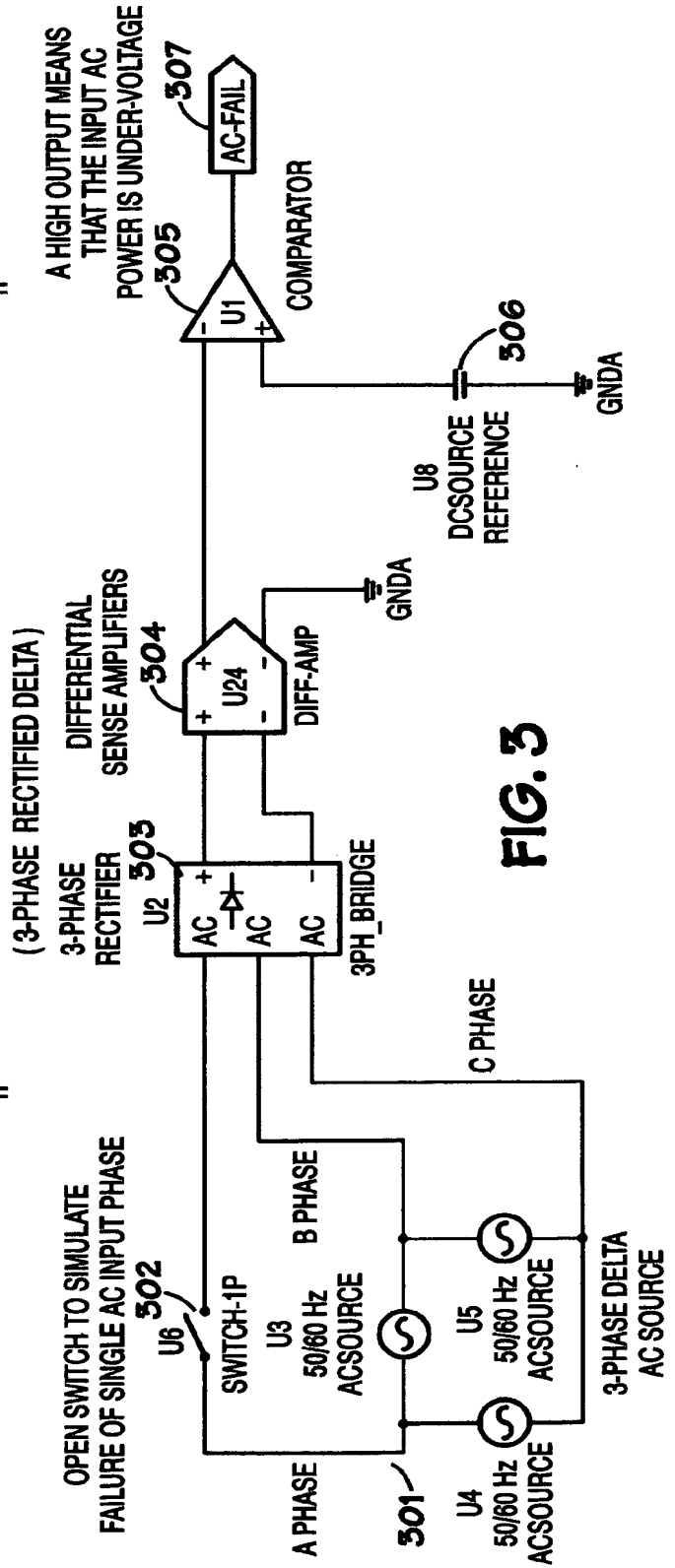
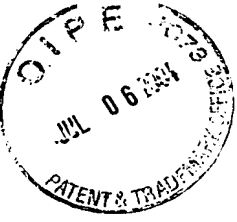


FIG. 3a

FIG. 3b





4

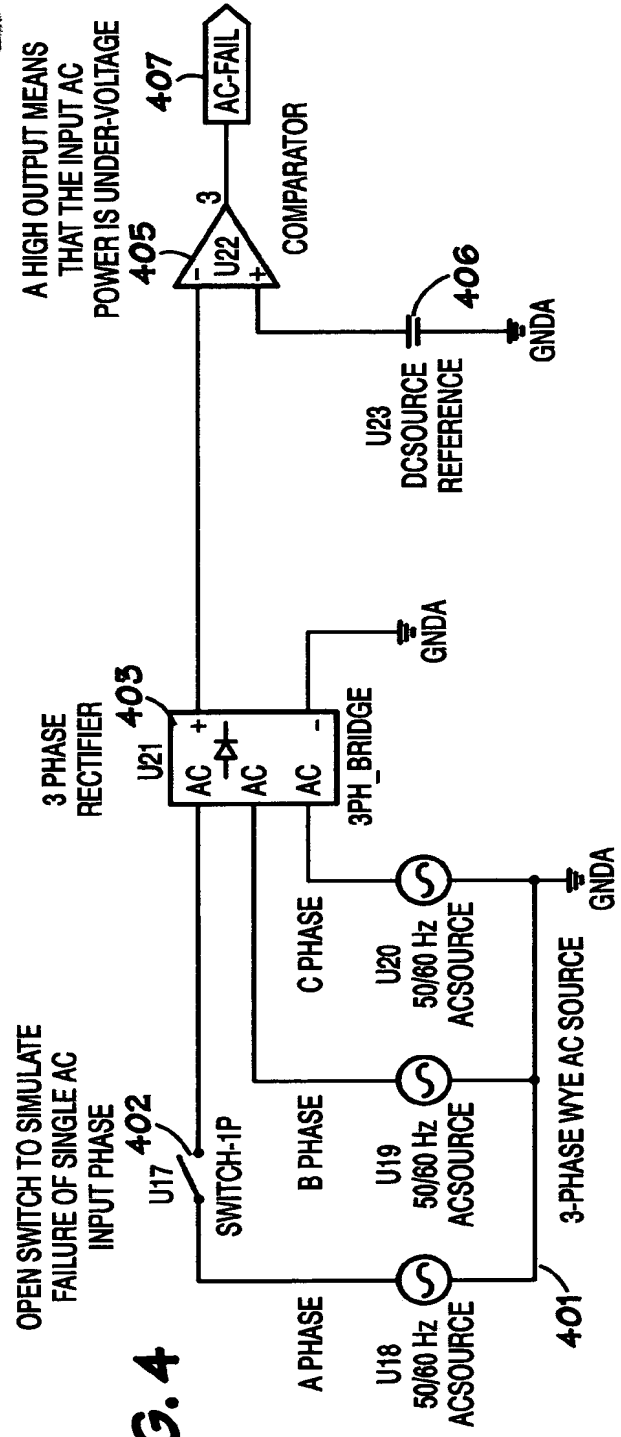


FIG. 4

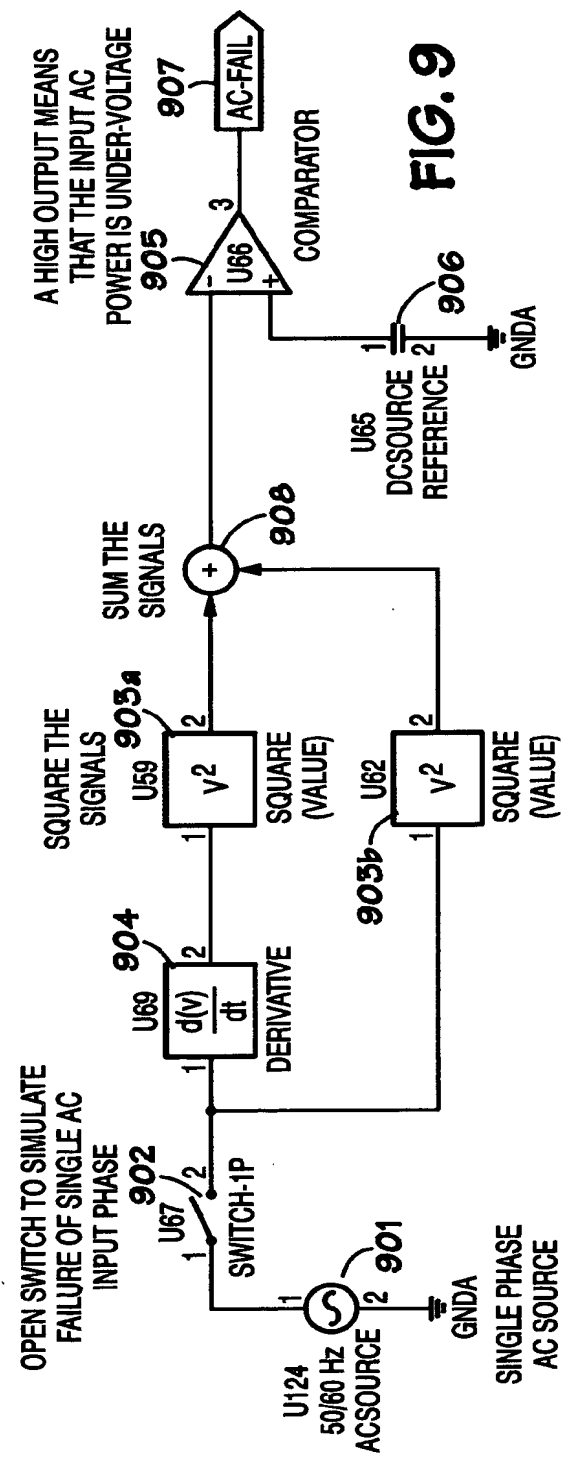
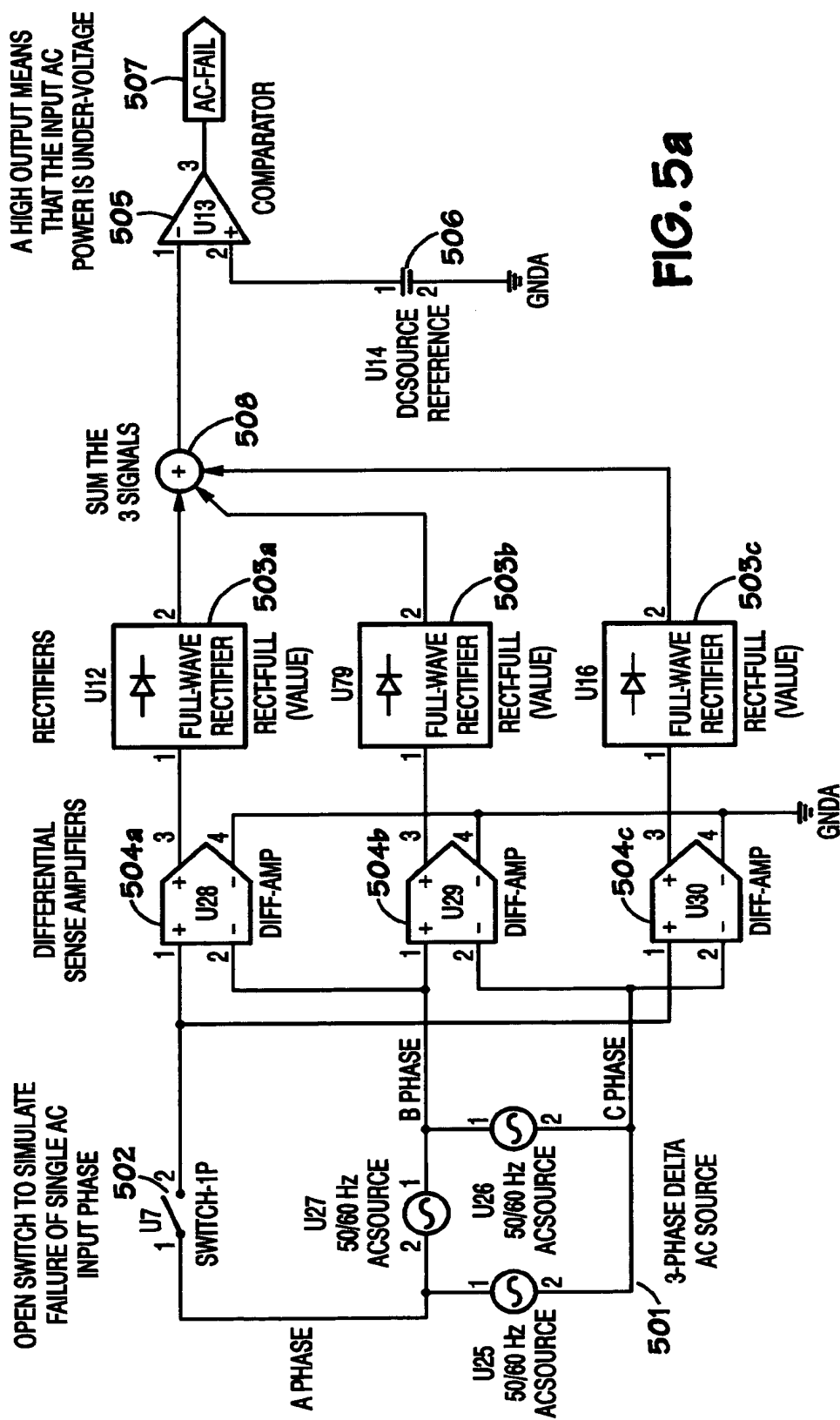


FIG. 9

4



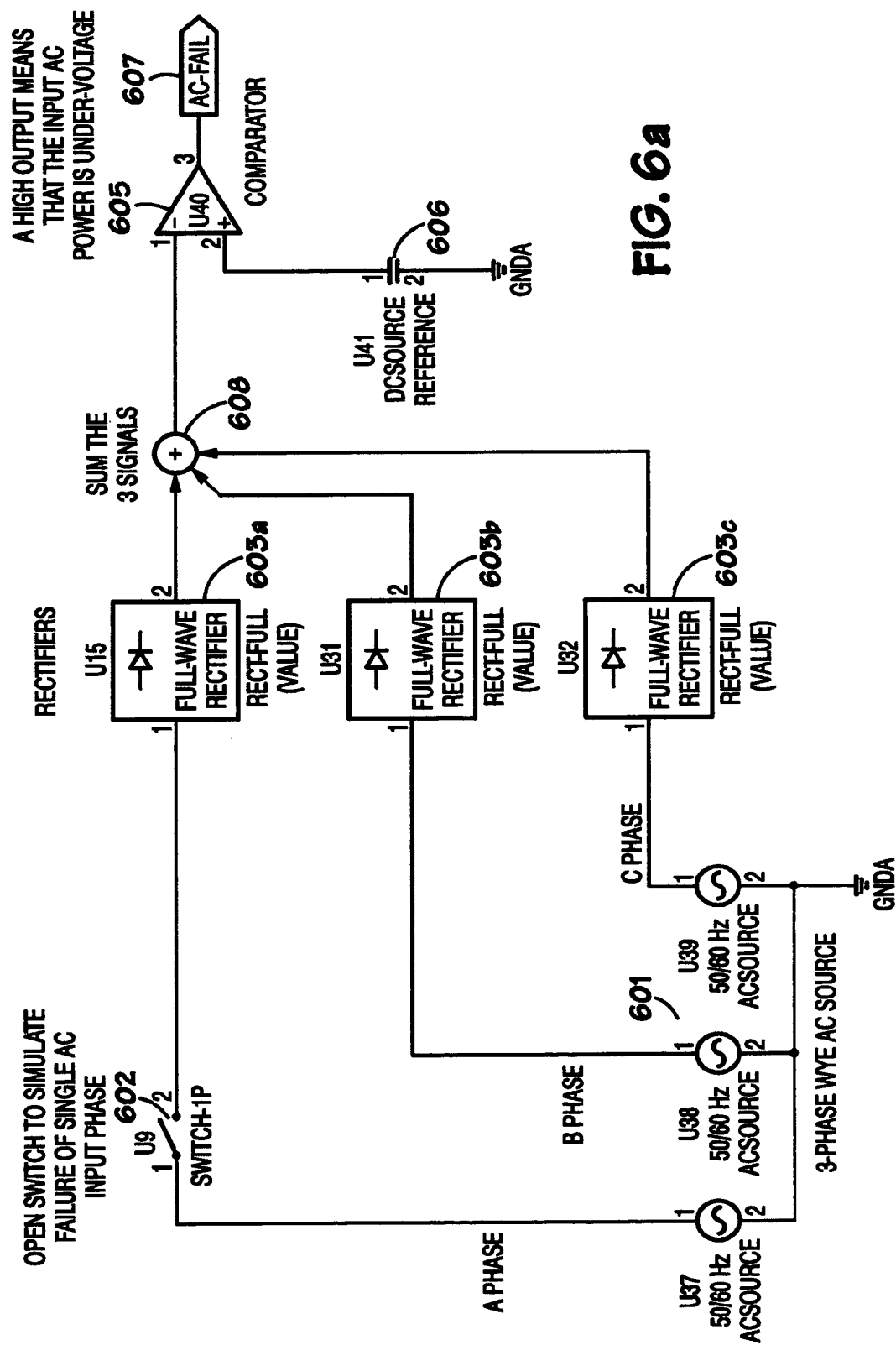
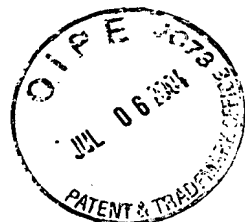


FIG. 6a



4

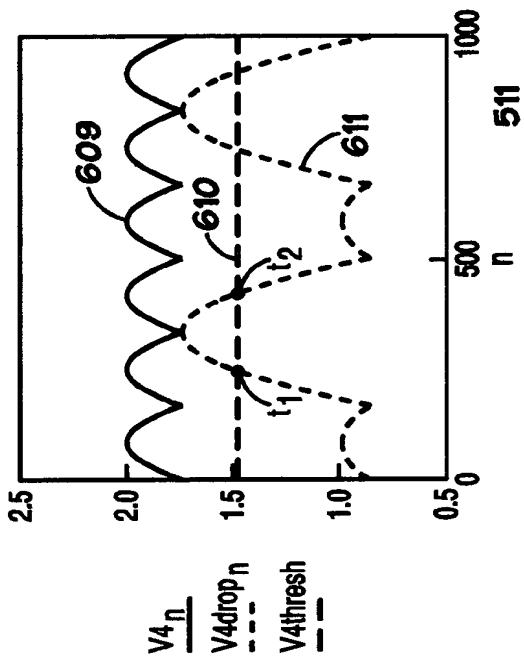


FIG. 6b

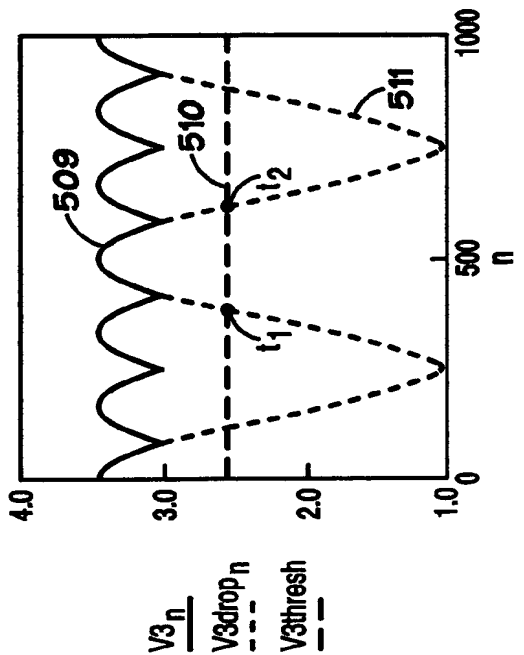
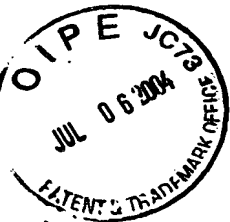


FIG. 5b



4

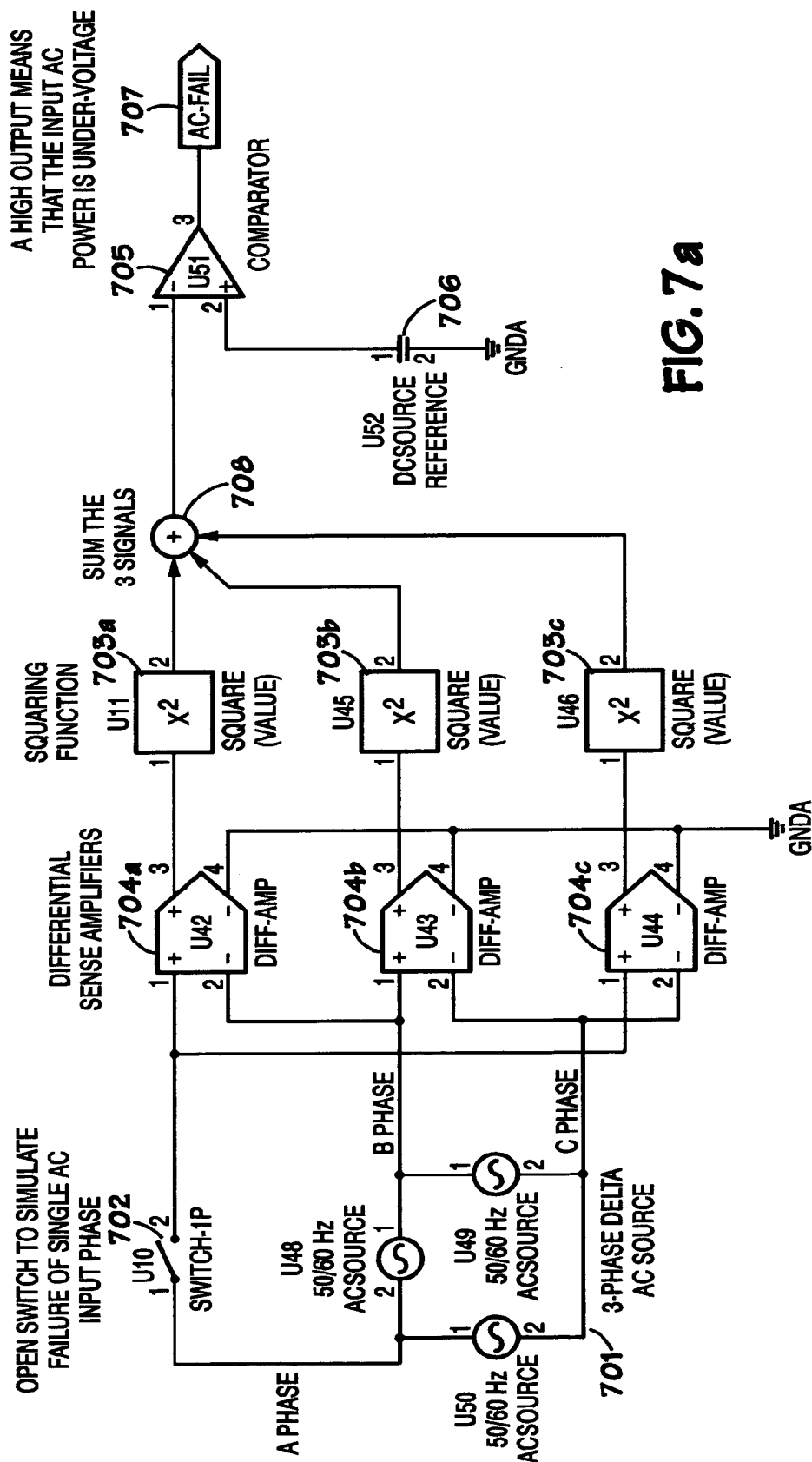


FIG. 7a

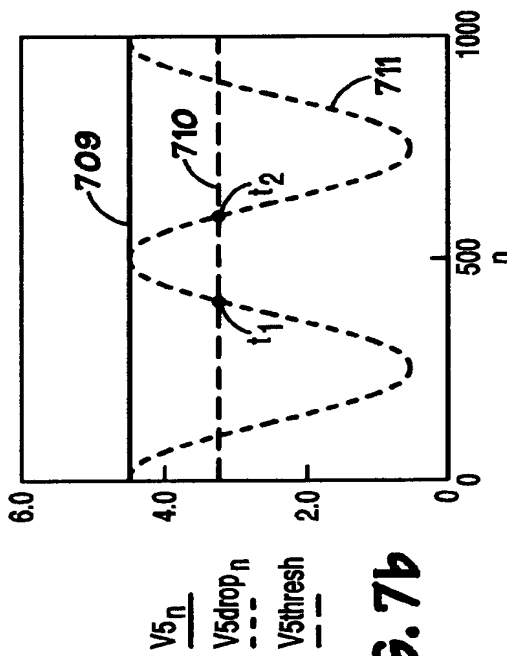
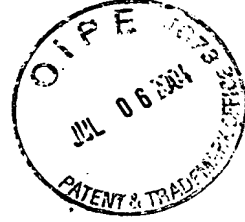


FIG. 7b

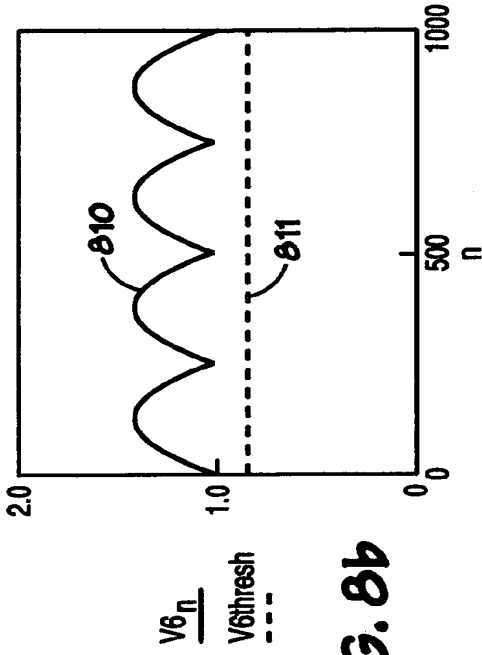


FIG. 8b

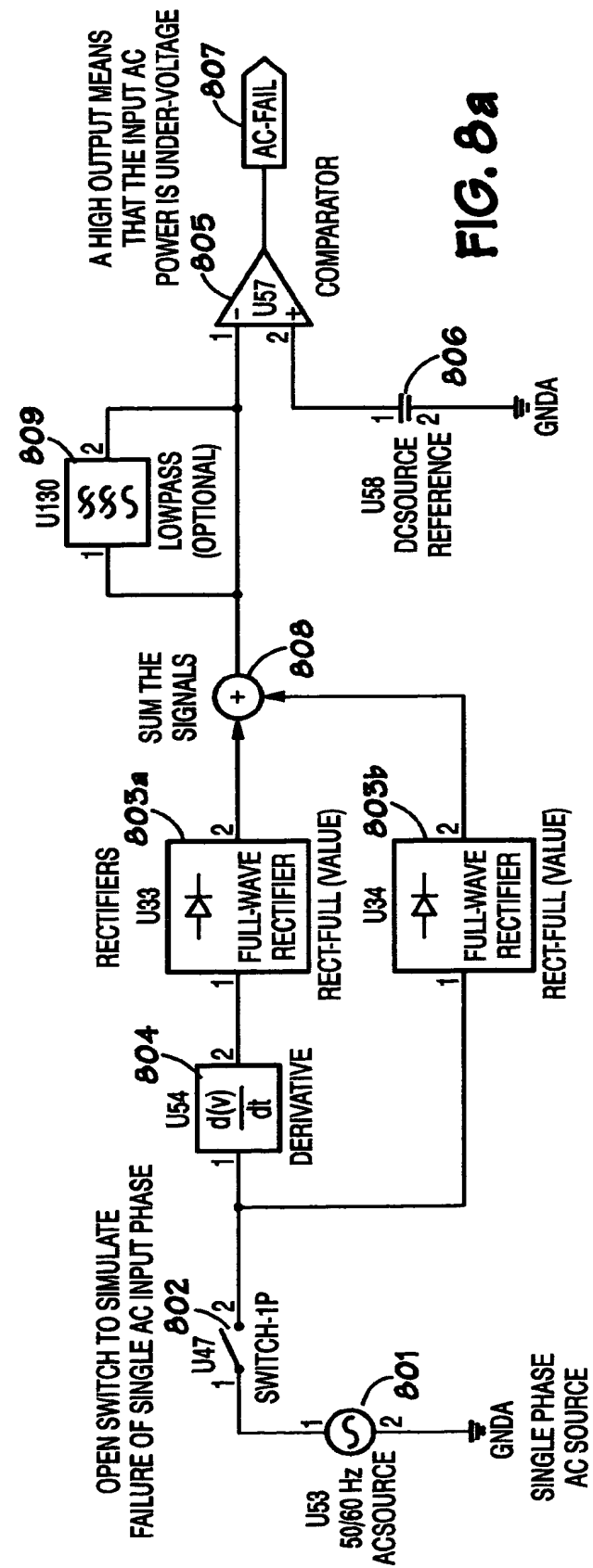
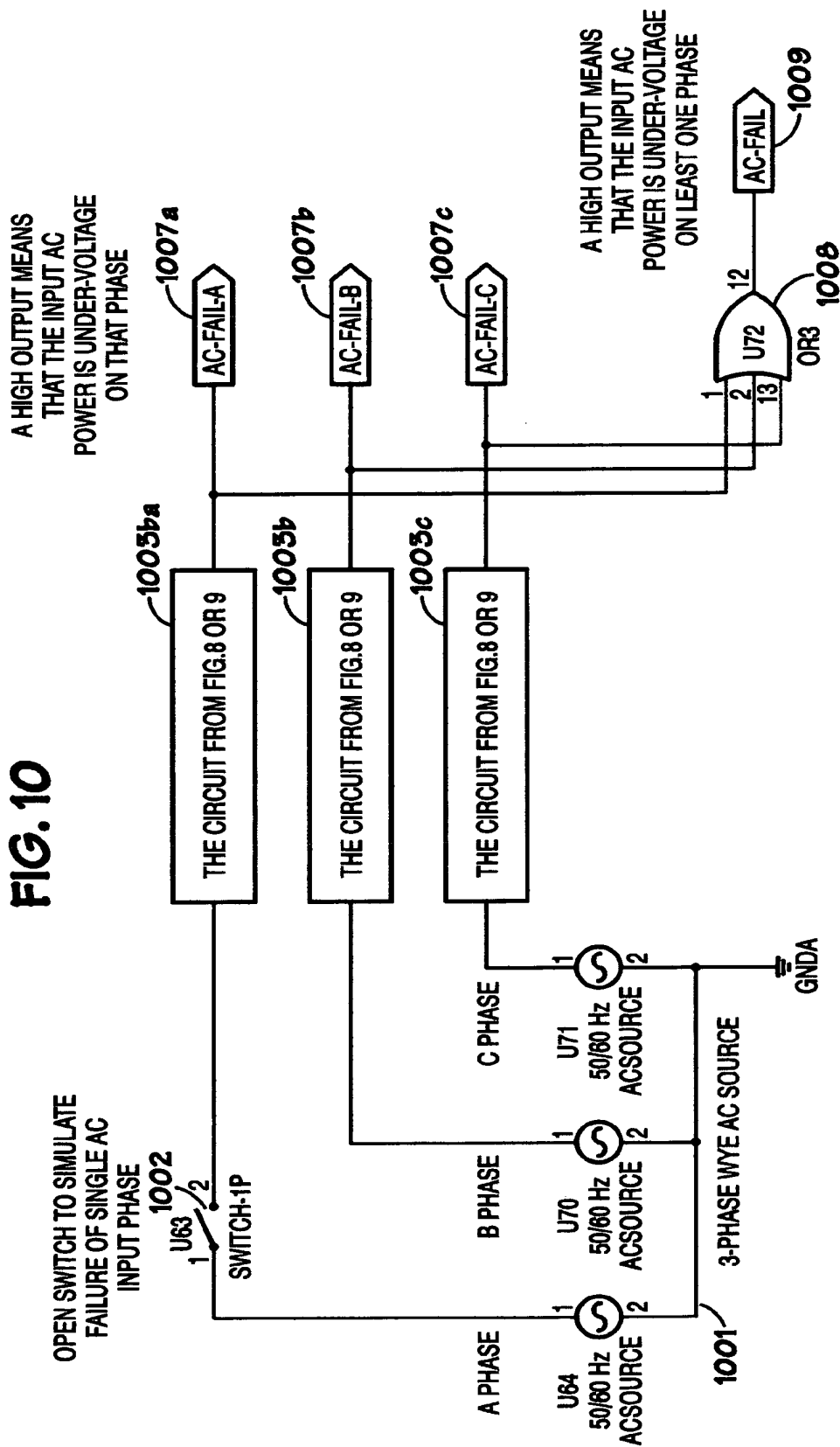


FIG. 8a





FIG. 10





4

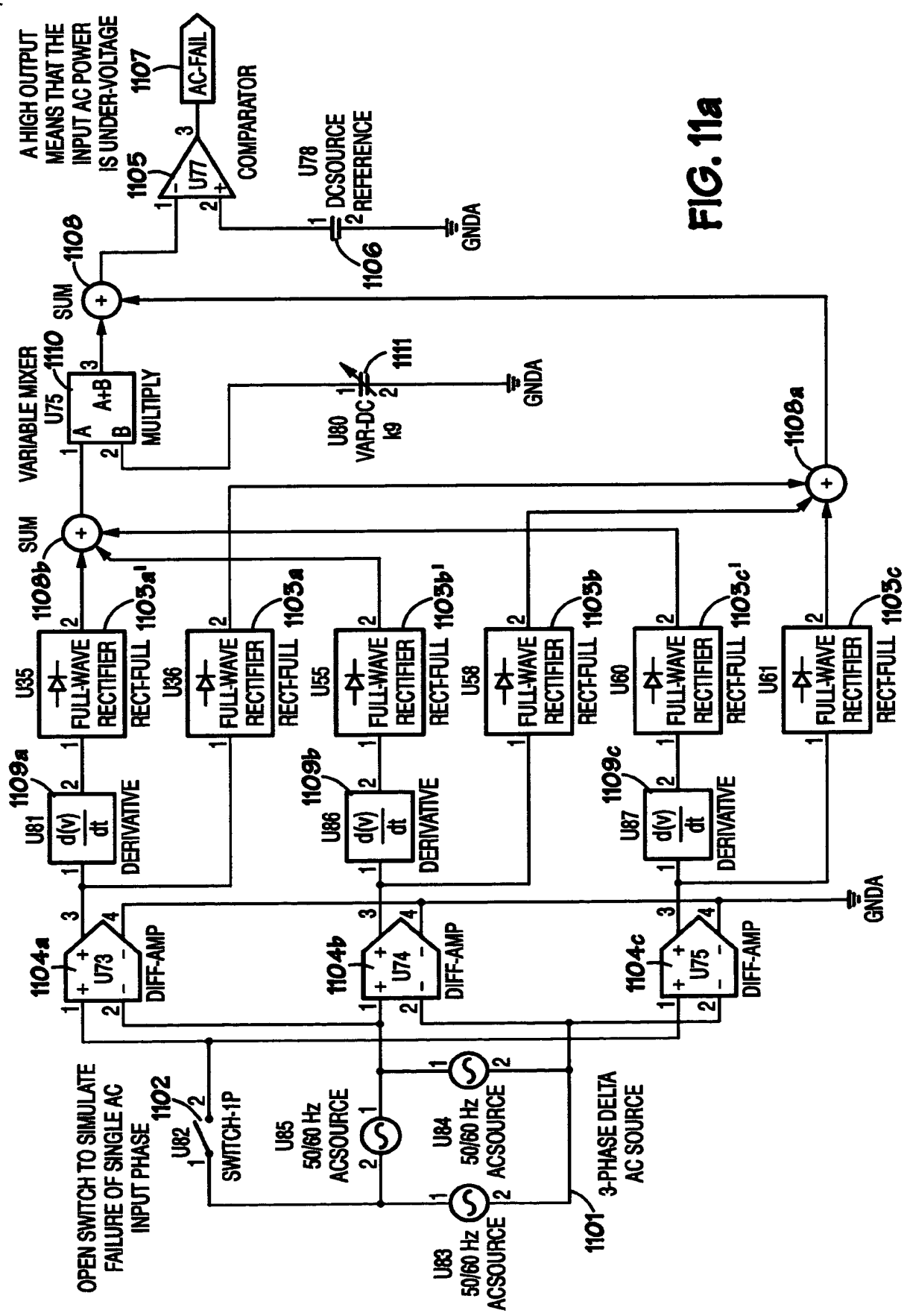
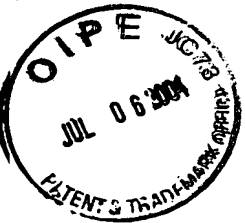
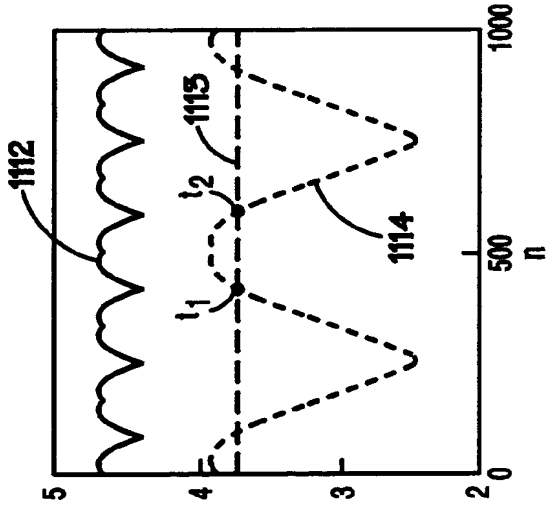


FIG. 11a

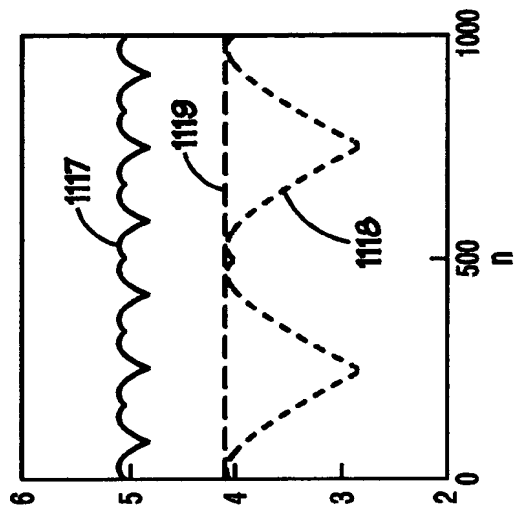


4



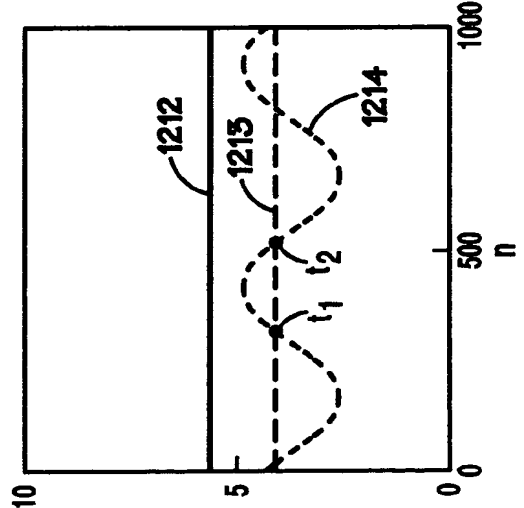
$V_{g_n}$   
 $V_{gdrop_n}$   
 $V_{gthresh}$

FIG. 11b



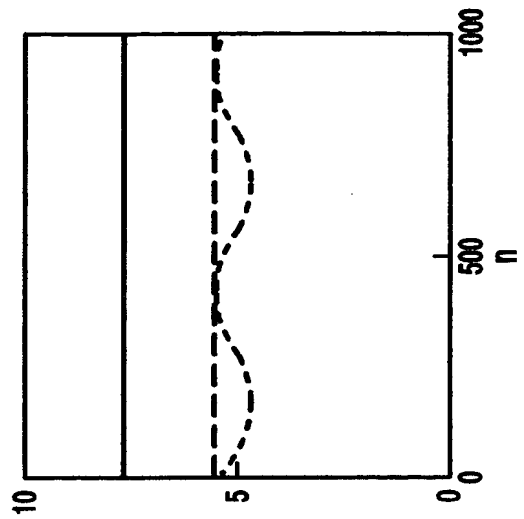
$V_{g_n}$   
 $V_{gdrop_n}$   
 $V_{gthresh}$

FIG. 11c



$V_{10_n}$   
 $V_{10drop_n}$   
 $V_{10thresh}$

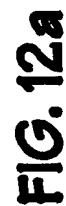
FIG. 12b

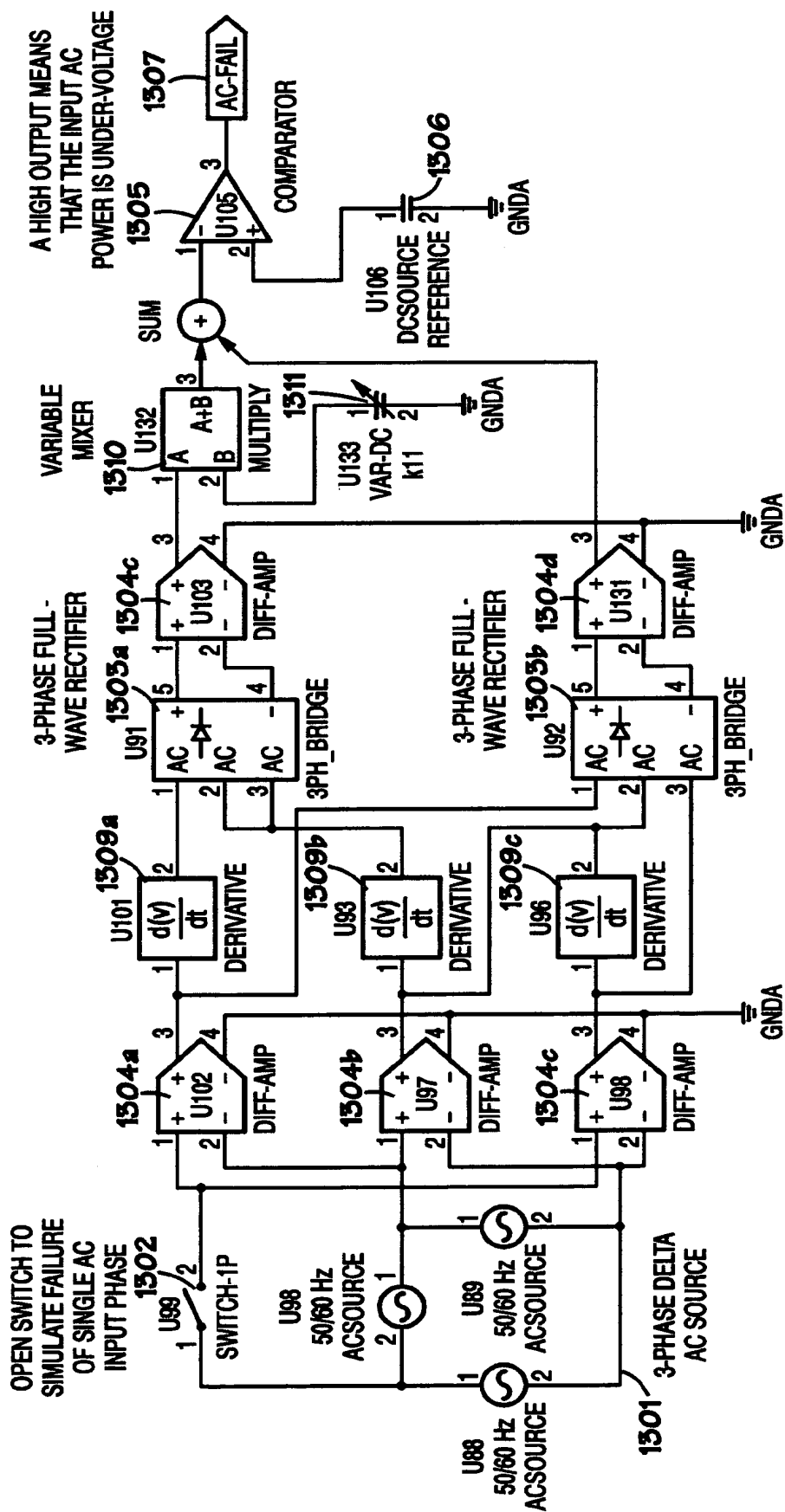


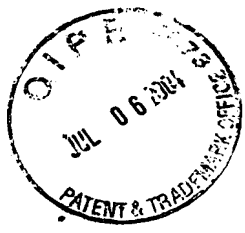
$V_{10_n}$   
 $V_{10drop_n}$   
 $V_{10thresh}$

FIG. 12c

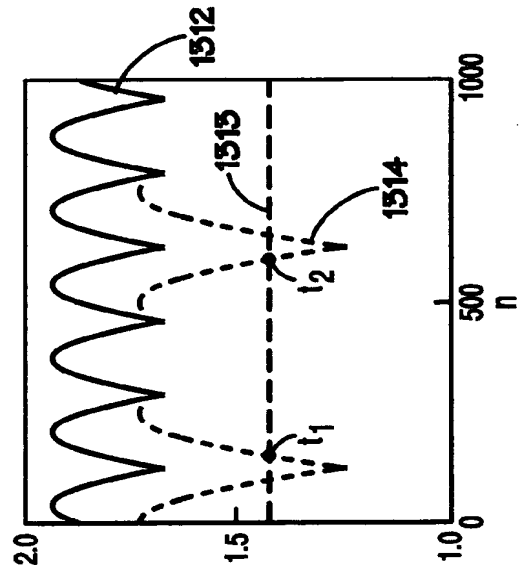
4







4



**FIG. 13b**

$V_{11n}$   
 $V_{11dropn}$   
 $V_{11thresh}$

